TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

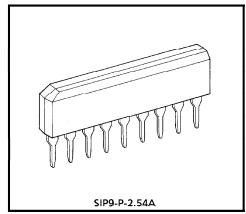
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DUAL PRE-AMPLIFIER

The TA8125S is dual output preamplifier designed for car or home use.

FEATURES

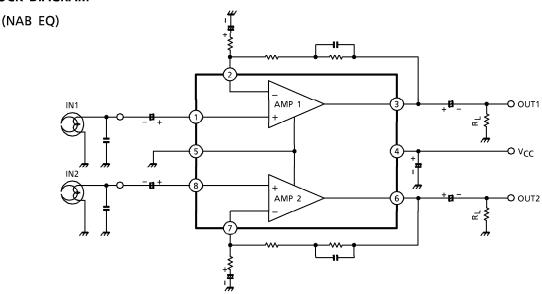
- High Open Loop Voltage Gain
 - : $G_{VO} = 100 dB$ (Typ.) at f = 1 kHz
- Excellent Channel Separation and High Ripple Rejection
 - : $CH_{sep} = 65dB$ (Typ.)
 - $(f = 10kHz, R_g = 2.2k\Omega, V_{OUT} = 0.775V_{rms} (0dBm))$
 - : R.R. = 50dB (Typ.)
 - $(f_{ripple} = 100Hz, R_g = 2.2k\Omega, V_{OUT} = 0.775V_{rms} (0dBm))$
- Low Noise
 - : $V_{NI} = 1.0 \mu V_{rms}$ (Typ.) at $R_q = 2.2 k\Omega$, $BW = 20 Hz \sim 20 kHz$,



Weight: 0.92g (Typ.)

Wide Operating Supply Voltage Range : $V_{CC (opr.)} = 6 \sim 16V (Ta = 25^{\circ}C)$

BLOCK DIAGRAM



- TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. the tist he responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.
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- The information contained herein is subject to change without notice.

MAXIMUM RATINGS (Ta = 25° C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	16	V
Power Dissipation (Note)	PD	700	mW
Operating Temperature	T _{opr}	- 30~80	°C
Storage Temperature	T _{stg}	- 55∼150	°C

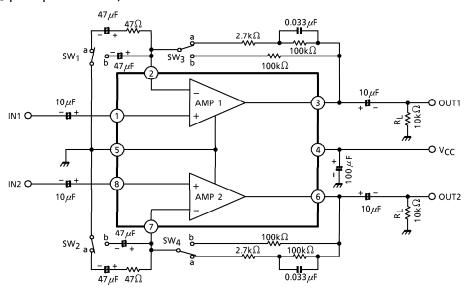
(Note) Derated above $Ta = 25^{\circ}C$ in the proportion of $5.6 \text{mW}/^{\circ}C$.

ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, V_{CC} = 6V, f = 1kHz, R_g = 600 Ω , R_L = 10k Ω , Ta = 25°C)

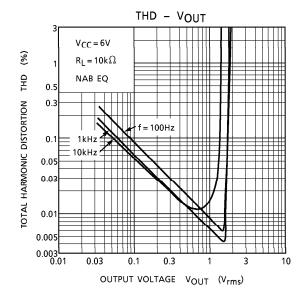
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CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Quiescent Current	lccQ	_	V _{IN} = 0	_	3	6	mA
Voltage Gain	GVO	_	$V_{OUT} = 7.75 \mu V_{rms} (-100 dBm)$	75	100		dB
	Gγ	<u> </u>	$V_{OUT} = 0.775V_{rms}$ (0dBm)	38.5	41.5	44.5	
Maximum Output Voltage	Vом	_	THD = 1%	1.0	1.8	_	V
Equivalent Input Noise Voltage	V _{NI}	_	$R_g = 2.2k\Omega$, B.W = 20Hz~20kHz	_	1.0	1.7	μ V $_{rms}$
Input Resistance	RIN	—	_	50	150	_	kΩ
Total Harmonic Distortion	THD	_	$V_{OUT} = 0.775V_{rms}$ (0dBm)	_	0.04	0.25	%
Channel Separation	CH _{sep}	_	f = 10kHz, V _{OUT} = 0.775V _{rms} (0dBm)	_	65	_	dB
Ripple Rejection Ratio	R.R.	_	$f = 100Hz$, $R_g = 2.2k\Omega$	_	50	_	dB

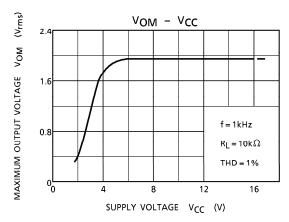
TEST CIRCUIT (9pin open or GND)

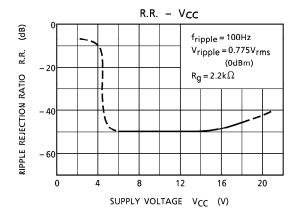


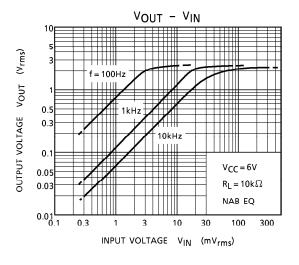
PIN9 : NC

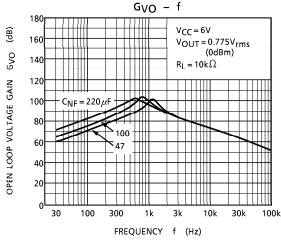
(*) $G_{VO} : SW_1 \sim SW_4 \rightarrow b SIDE$

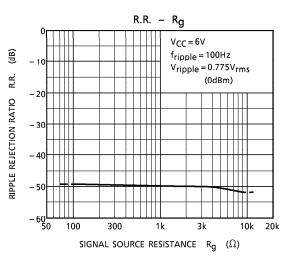


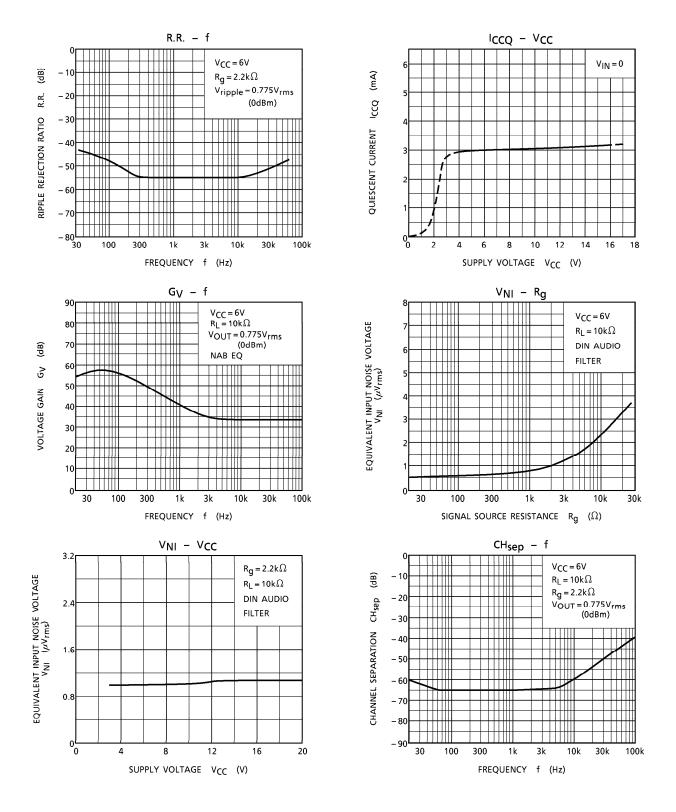


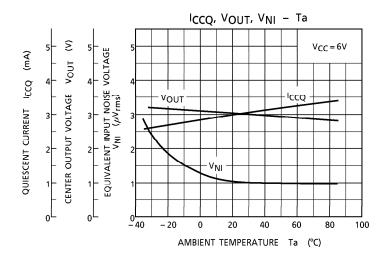


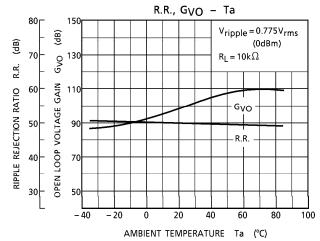


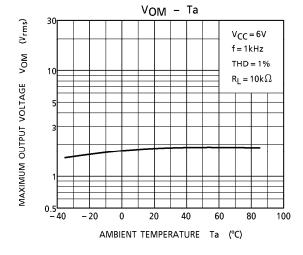


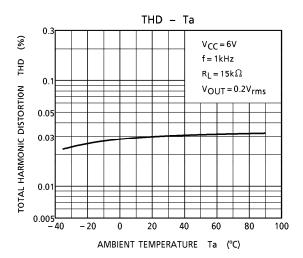




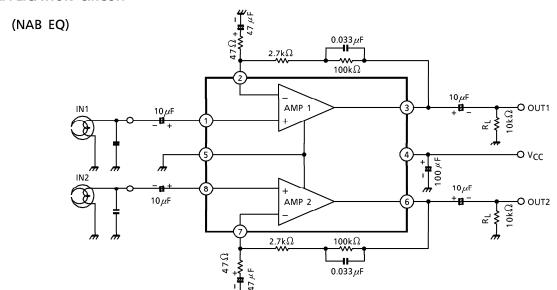






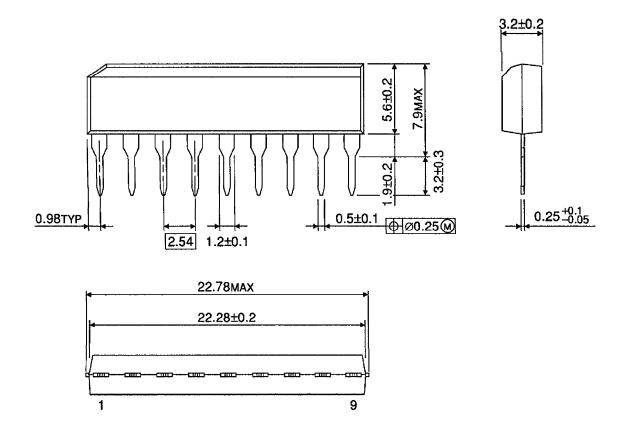


APPLICATION CIRCUIT



OUTLINE DRAWING

SIP9-P-2.54A Unit: mm



Weight: 0.92g (Typ.)